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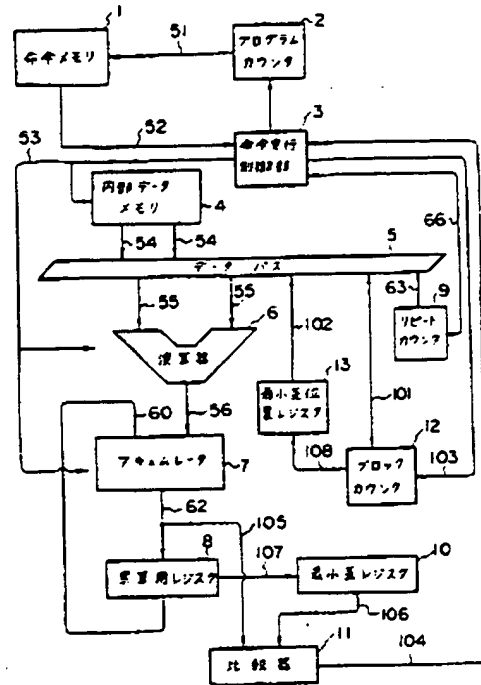
APPLICATION DATE : 16-12-88
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APPLICANT : MITSUBISHI ELECTRIC CORP;

INVENTOR : OHIRA HIDEO;

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TITLE : DIGITAL SIGNAL PROCESSING PROCESSOR



ABSTRACT : PURPOSE: To save useless operation to suppress required arithmetic quantity effectively and to make the processing time efficient by adopting the constitution such that a minimum distortion is compared with the result of calculated distortion for each cycle at the calculation of distortion between blocks and a number of a block giving the minimum distortion is stored.

CONSTITUTION: The result of accumulation supplied from an output path 62 of an accumulator 7 via a branched output path 105 and an output data supplied from a minimum distortion register 10 via an output path 106 are compared for each cycle by a comparator 11. The accumulation is executed for a number of times provided in a repeat counter 9 and when the accumulation is finished normally, the write update of a value of an accumulation register 8 to a minimum distortion register 10, the write update of a value of a block counter 12 to a minimum distortion position register 13 and the control of the block counter 12 by an increment control signal are implemented.

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